

8-Bit Single-Chip Microcontrollers

SDA 2043
SDA 2063
SDA 2083

Preliminary Data

MOS IC

Features

- 8051 CPU
- Cycle time 1 μ s at 12 MHz
- Supply voltage 5 V
- 4/6/8-Kbyte ROM
- 128-byte RAM
- Integrated I²C bus interface
- 34 bidirectional I/O lines
- Input for directly processing IR signals
- Proven NMOS technology
- P-DIP-40 package
- Special devices like piggyback PIG 30... available for program development

The three devices SDA 2043/2063/2083 are pin and functionally compatible and differ solely in the size of their program memory. This permits custom adaptation to system requirements. The microcomputer "grows along" with the application. Applications with greater program memory can be handled by other ICs (**see table**).

There is a suitable emulator and piggyback available for these devices for generating software or creating prototypes. Enquire for further details.

Type	Ordering Code	Package
SDA 2043 (A XXX)	Q67120-C449	P-DIP-40 (4 Kbytes)
SDA 2063 (A XXX)	Q67120-C450	P-DIP-40 (6 Kbytes)
SDA 2083 (A XXX)	Q67120-C451	P-DIP-40 (8 Kbytes)

Note

The above devices can only be supplied with an application-specific program (ROM). The designation for this program is A XXX. Orders for devices without this program designation cannot be accepted.